

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A transceiver for a transmission and reception signal which can be transmitted via a signal line having a particular line impedance, having comprising:
 - a) a line driver for driving a transmission signal via the signal line, the line driver having a synthesized output impedance; and having
 - b) [[an]] a programmable analog echo cancellation filter for signal suppression for an echo signal brought about by the transmission signal, ~~wherein the line driver has a synthesized output impedance, with the line driver having a downstream;~~
 - c) a hybrid circuit for connecting [[an]] said analog echo cancellation filter to the signal line;
 - d) a reception filter for filtering a signal received via the signal line; and
 - e) a subtraction circuit which subtracts from the filtered output signal of the reception filter the transmission signal simulated by the echo cancellation filter to generate a reception signal liberated of the echo signal.
2. (Cancelled)
3. (Currently Amended) The transceiver as claimed in claim [[2]] 1, wherein the transfer function of the echo cancellation filter has a programmable pole point and a programmable zero point.
4. (Previously Presented) The transceiver as claimed in claim 1, wherein the line driver is of differential design.
5. (Currently Amended) The transceiver as claimed in claim 1, wherein the synthesized output impedance of the line driver [[(6)]] is real.
6. (Previously Presented) The transceiver as claimed in claim 1, wherein the hybrid circuit has a first two-pole connection next to the output of the line driver, a

second two-pole connection for connection to the signal line, and a third two-pole connection for connection to the analog echo cancellation filter.

7. (Previously Presented) The transceiver as claimed in claim 6, wherein the first connection on the hybrid circuit and the second connection on the hybrid circuit have series resistors between them which are connected in series with the line impedance of the signal line.

8. (Previously Presented) The transceiver as claimed in claim 6, wherein the first connection on the hybrid circuit and the second connection on the hybrid circuit respectively have a first and a second series-connected crosscoupling resistor between them.

9. (Previously Presented) The transceiver as claimed in claim 6, wherein the third two-pole connection on the hybrid circuit for connecting the echo cancellation filter is tapped off between the series-connected crosscoupling resistors.

10. (Previously Presented) The transceiver as claimed in claim 6, wherein the resistance values of the resistors connected in the hybrid circuit satisfy the following equation:

$$R_2 = R_3 \cdot \frac{R_{SYN}}{R_1 + R_{SYN}}$$

where R_1 is the resistance value of a series resistor, R_2 is the resistance value of the first crosscoupling resistor, and R_3 is the resistance value of the second crosscoupling resistor, and where R_{SYN} is the synthesized output impedance of the line driver.

11. (Previously Presented) The transceiver as claimed in claim 6, wherein the resistors connected in the hybrid circuit are real resistors.

12. (Previously Presented) The transceiver as claimed in claim 6, wherein the hybrid circuit is of symmetrical design.

13. (Cancelled)

14. (Cancelled)

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15. (Original) The use of the transceiver as claimed in claim 1 for a broadband communication system, particularly for an xDSL broadband communication system.